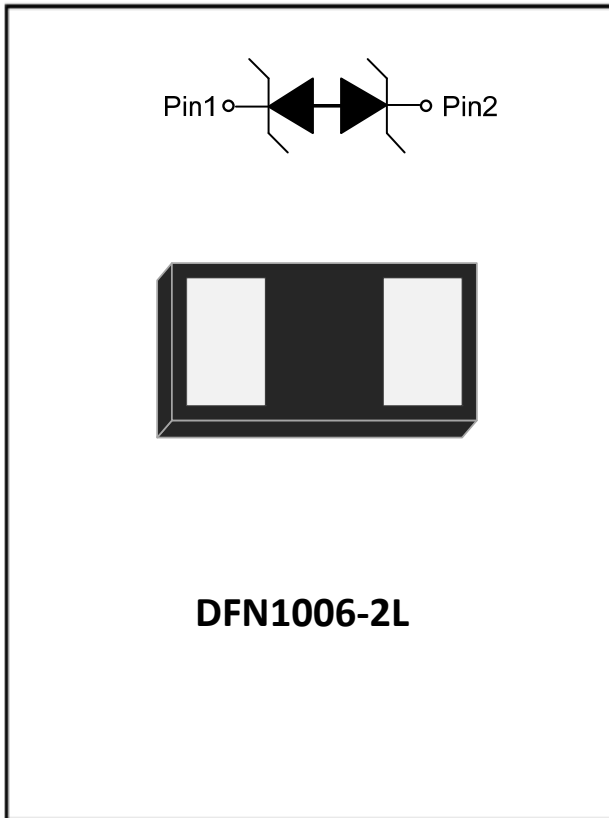


## 1-Line, Bi-directional, Transient Voltage Suppressor



### Features

- Stand-off voltage: 7V Max
- Transient protection for each line according to  
IEC61000-4-2(ESD):  $\pm 30\text{kV}$  (contact)  
IEC61000-4-5(surge): 6A (8/20 $\mu\text{s}$ )
- Low leakage current
- Low clamping voltage
- RoHS Compliant

### Applications

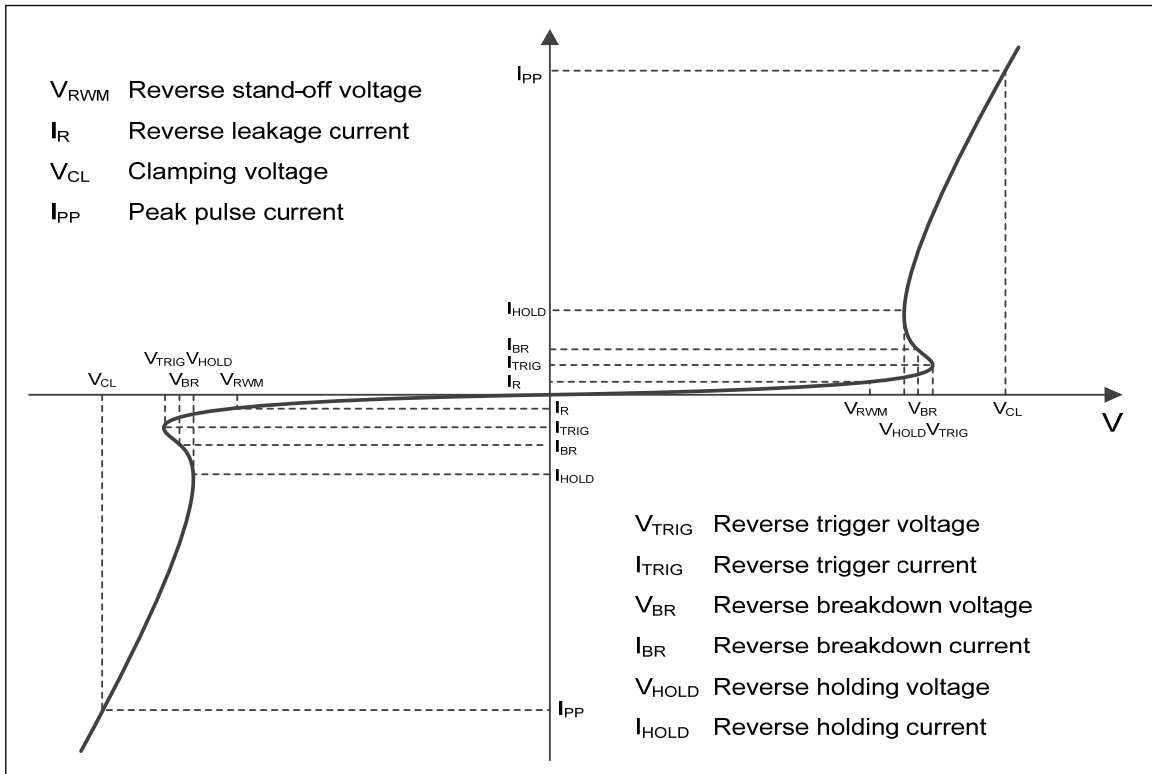
- Cellular handsets
- Tablets
- Laptops
- Network communication devices
- Other portable devices

### Mechanical Data

- Package: DFN1006-2L
- Case Material: "Green" Molding Compound
- Marking Information: See Below
- Marking:



### ■Definitions of electrical characteristics





# ESD7V0LB1

## ■Maximum Ratings

PARAMETER	SYMBOL	Rating	UNIT
Peak pulse power ( $t_p = 8/20\mu s$ )	$P_{pk}$	72	W
Peak pulse current ( $t_p = 8/20\mu s$ )	$I_{pp}$	6	A
ESD according to IEC61000-4-2 air discharge	$V_{ESD}$	$\pm 30$	KV
ESD according to IEC61000-4-2 contact discharge		$\pm 30$	KV
Junction temperature	$T_J$	125	$^{\circ}C$
Storage temperature	$T_{STG}$	-55~150	$^{\circ}C$

## ■Electrical Characteristics ( $T_a=25^{\circ}C$ Unless otherwise specified)

PARAMETER	Symbol	UNIT	Conditions	Min	Typ	Max
Reverse maximum working voltage	$V_{RWM}$	V				7
Reverse leakage current	$I_R$	nA	$V_{RWM} = 7V$			100
Reverse breakdown voltage	$V_{BR}$	V	$I_{BR} = 1mA$	7.2		10
Clamping voltage <sup>3)</sup>	$V_{CL}$	V	$I_{PP} = 1A, t_p = 8/20\mu s$			9
		V	$I_{PP} = 6A, t_p = 8/20\mu s$			12
Junction capacitance	$C_J$	pF	$V_R = 0V, f = 1MHz$		17	20

(1). TLP parameter:  $Z_0 = 50\Omega, t_p = 100ns, t_r = 2ns$ , averaging window from 60ns to 80ns.  $R_{DYN}$  is calculated from 4A to 16A.

(2). Contact discharge mode, according to IEC61000-4-2.

(3). Non-repetitive current pulse, according to IEC61000-4-5.

## ■Ordering Information (Example)

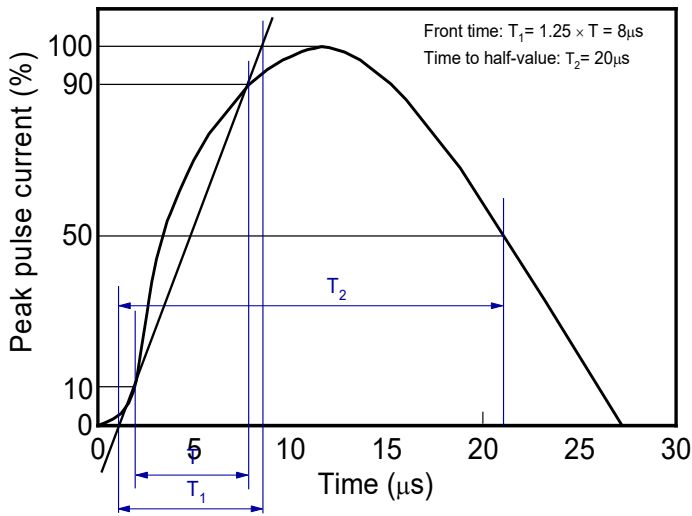
PREFERRED P/N	UNIT WEIGHT(mg)	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
ESD7V0LB1	Approximate 0.9	10000	100000	400000	Tape & reel



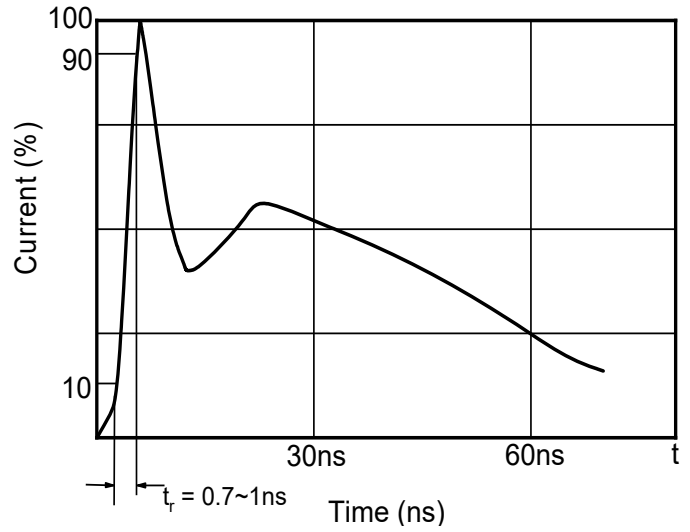
# ESD7V0LB1

## ■ Characteristics (Typical)

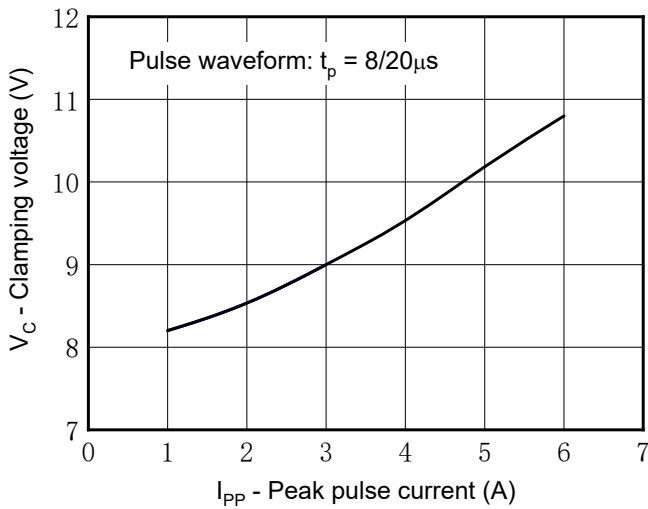
8/20 $\mu$ s waveform per IEC61000-4-5



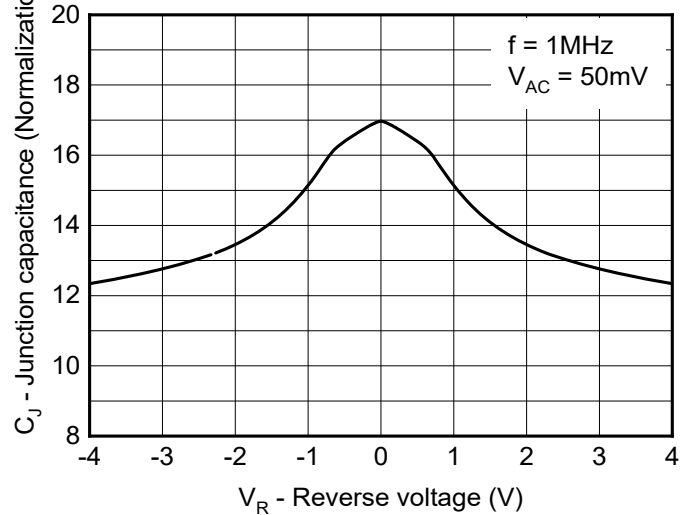
Contact discharge current waveform per IEC61000-4-2



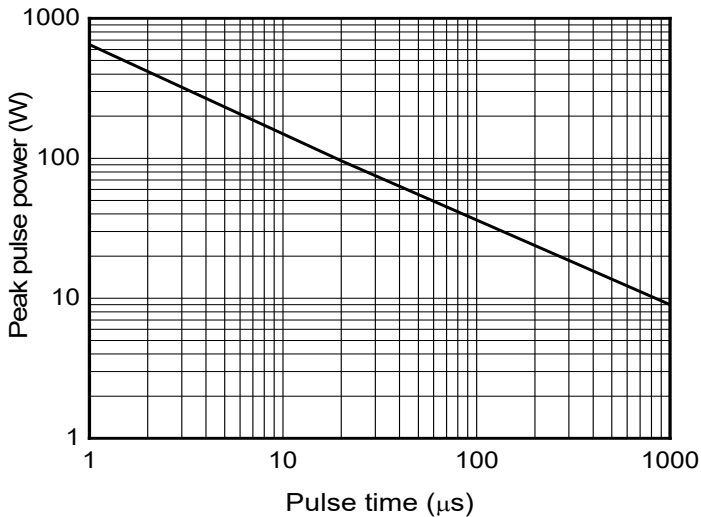
Clamping voltage vs. Peak pulse current



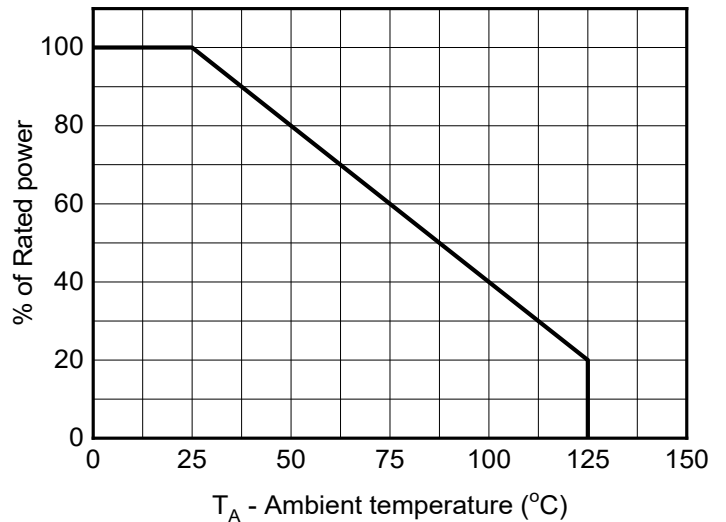
Capacitance vs. Reverse voltage



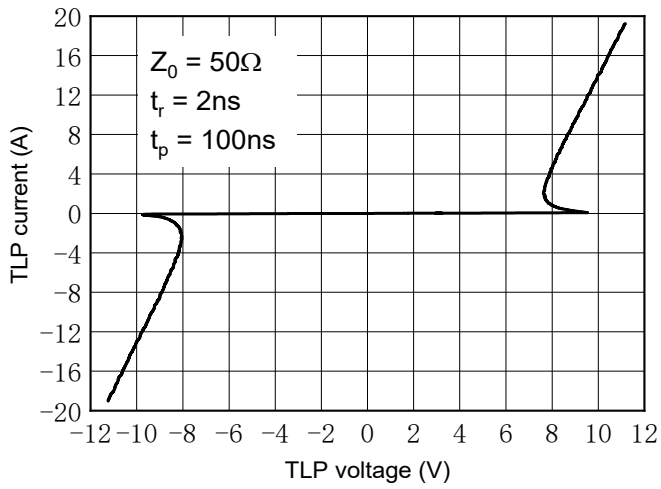
Non-repetitive peak pulse power vs. Pulse time



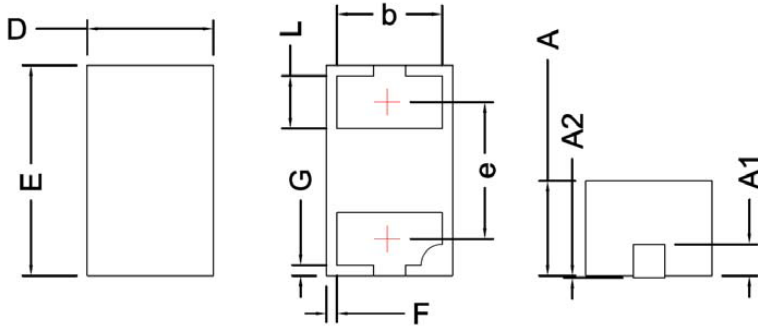
Power derating vs. Ambient temperature



## TLP Measurement

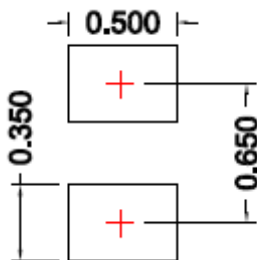


## ■ Outline Dimensions



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
D	0.50	0.60	0.70
E	0.90	1.00	1.10
A	0.35	0.45	0.55
A1	0.15 BSC		
A2			0.10
F	0.005		
G	0.005		
L	0.15	0.25	0.35
b	0.41	0.50	0.59
e	0.65 BSC		

## ■ Recommended PCB Layout



### Notes:

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met

Unit:mm



# ESD7V0LB1

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